

FIG. 1

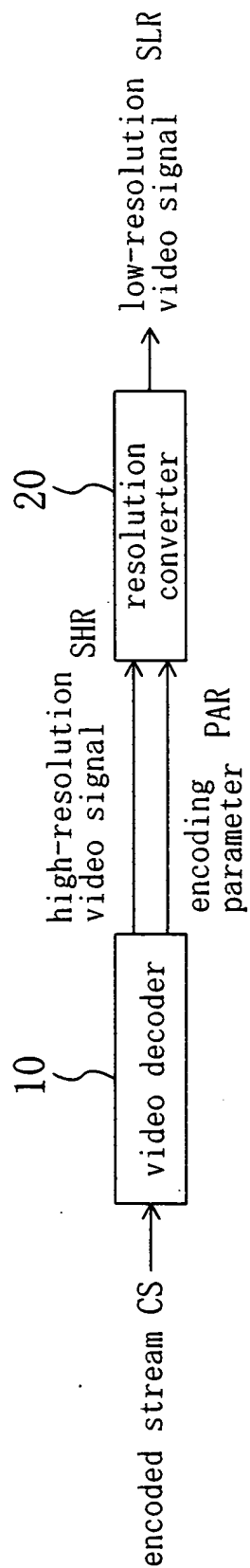


FIG. 2

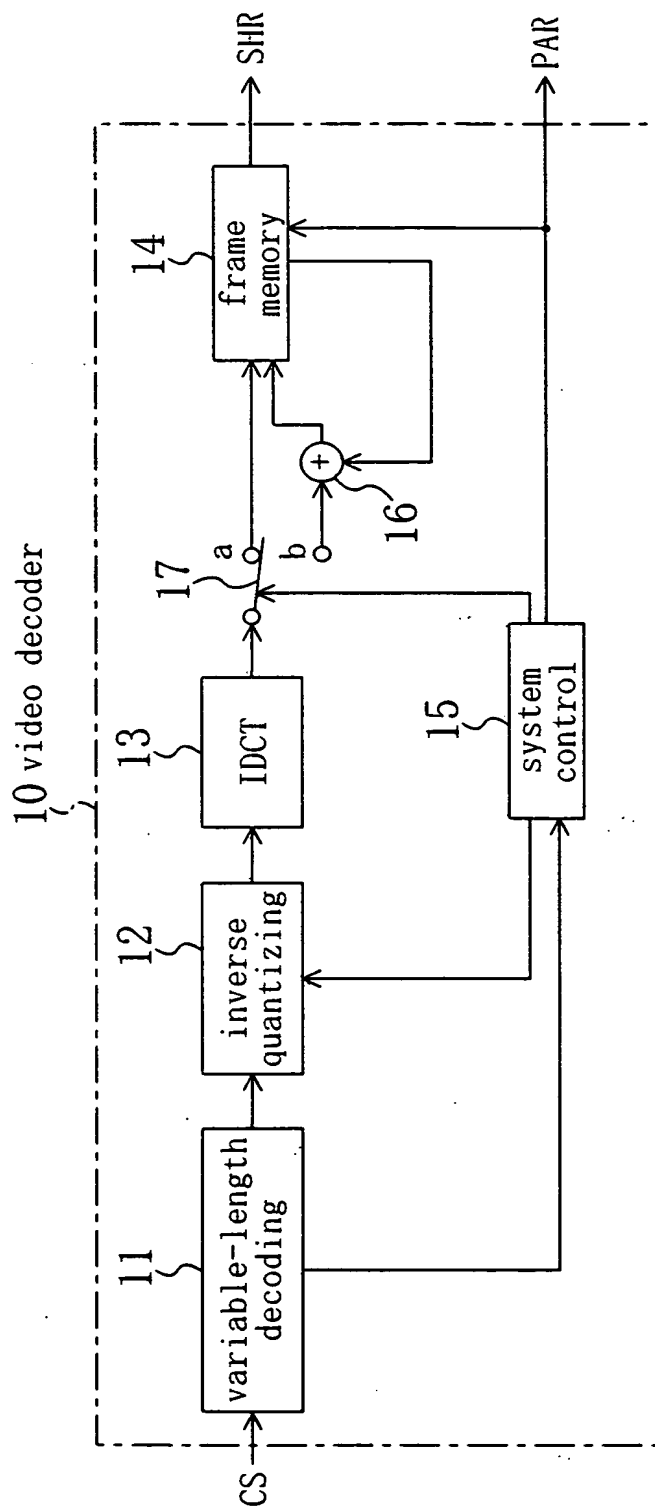
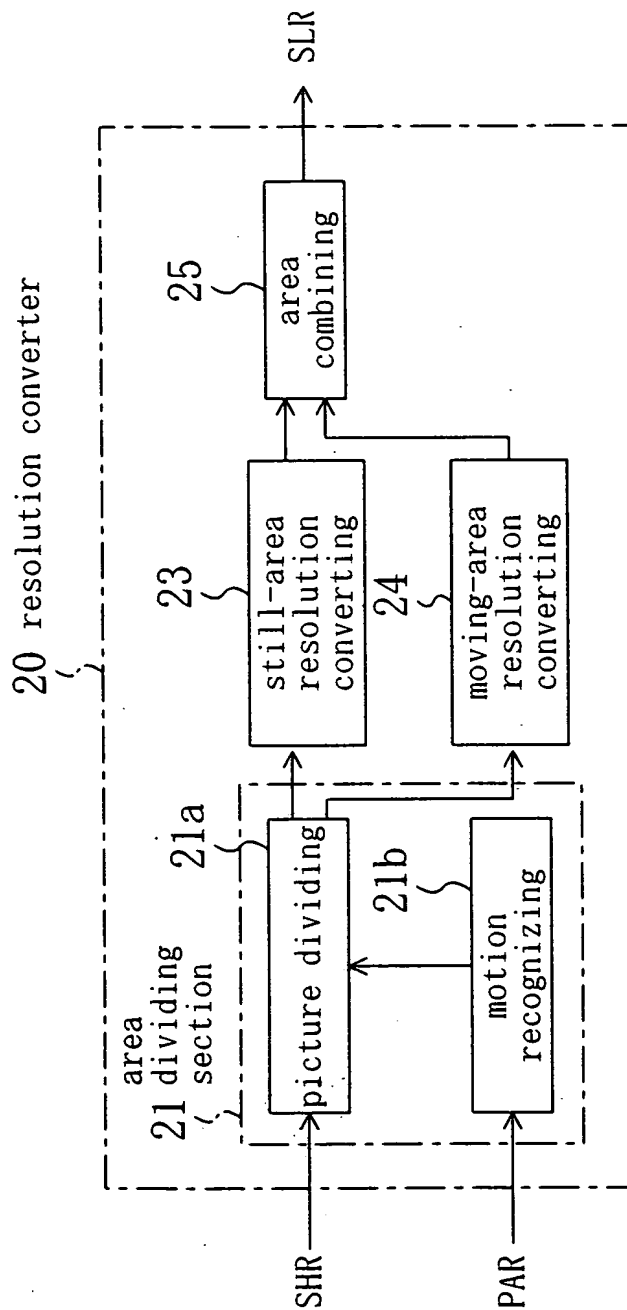
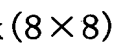


FIG. 3



macroblock (16×16)



block (8×8)

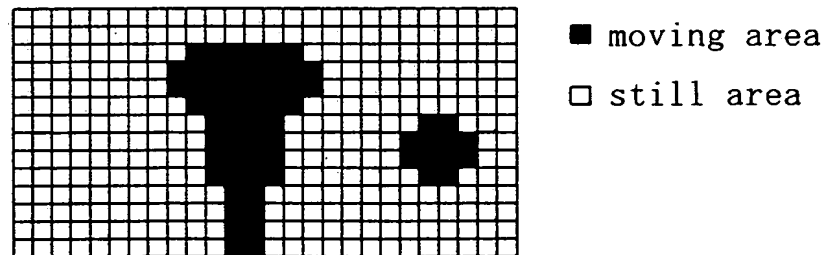
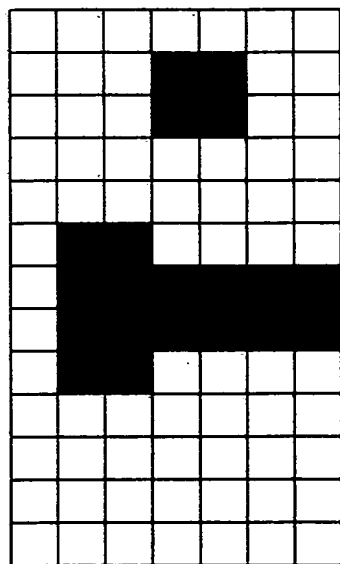


FIG. 5B



still
area \rightarrow

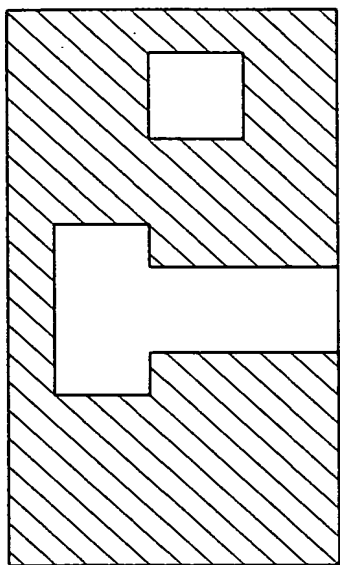


FIG. 5C

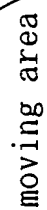


FIG. 6A

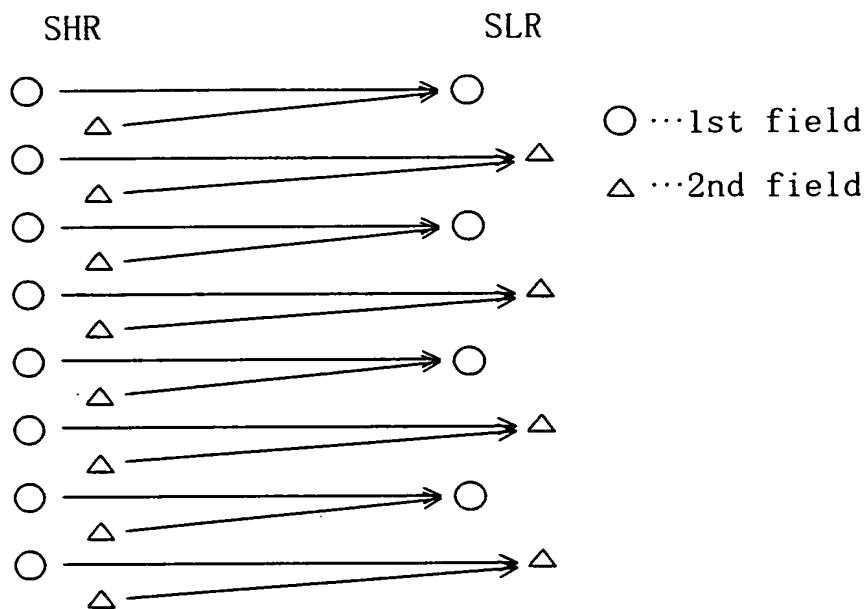


FIG. 6B

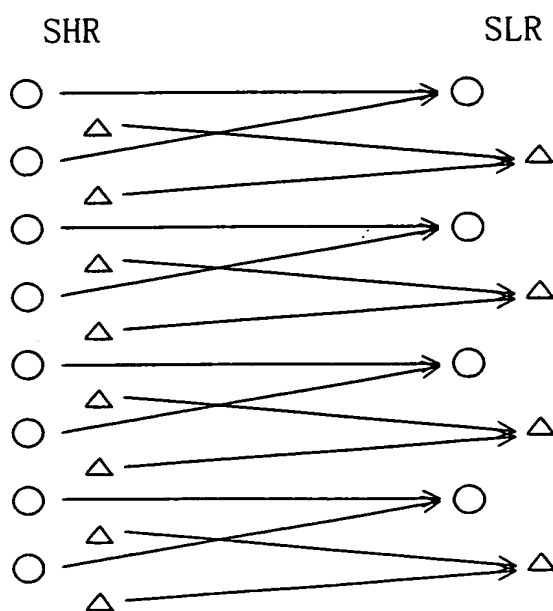


FIG. 7A

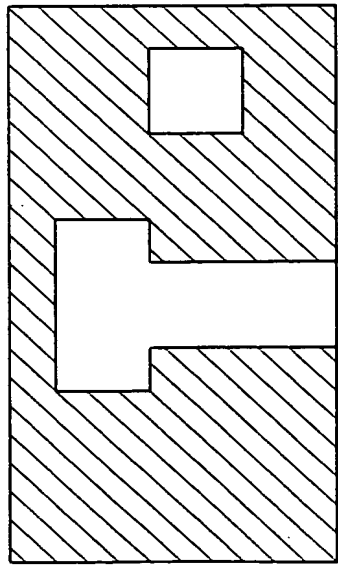


FIG. 7C

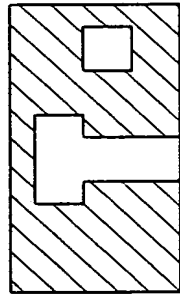


FIG. 7E

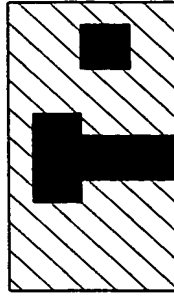


FIG. 7B



FIG. 7D

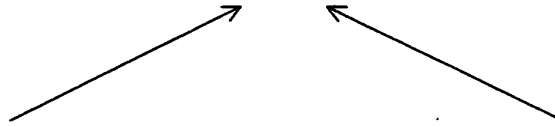


FIG. 8

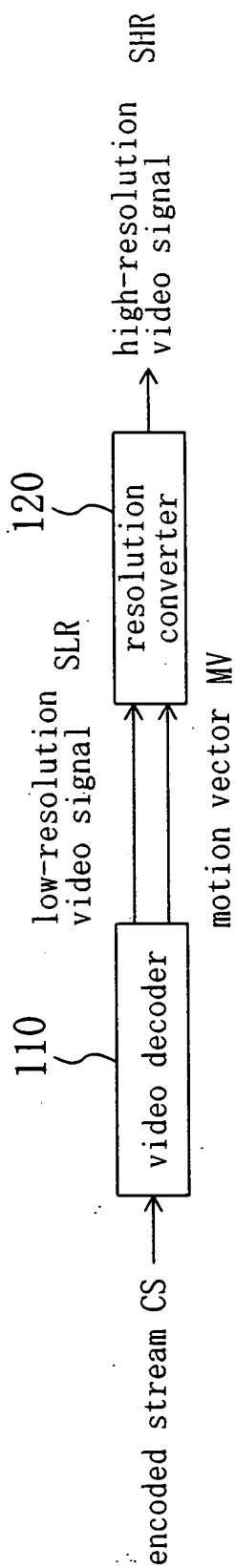


FIG. 9

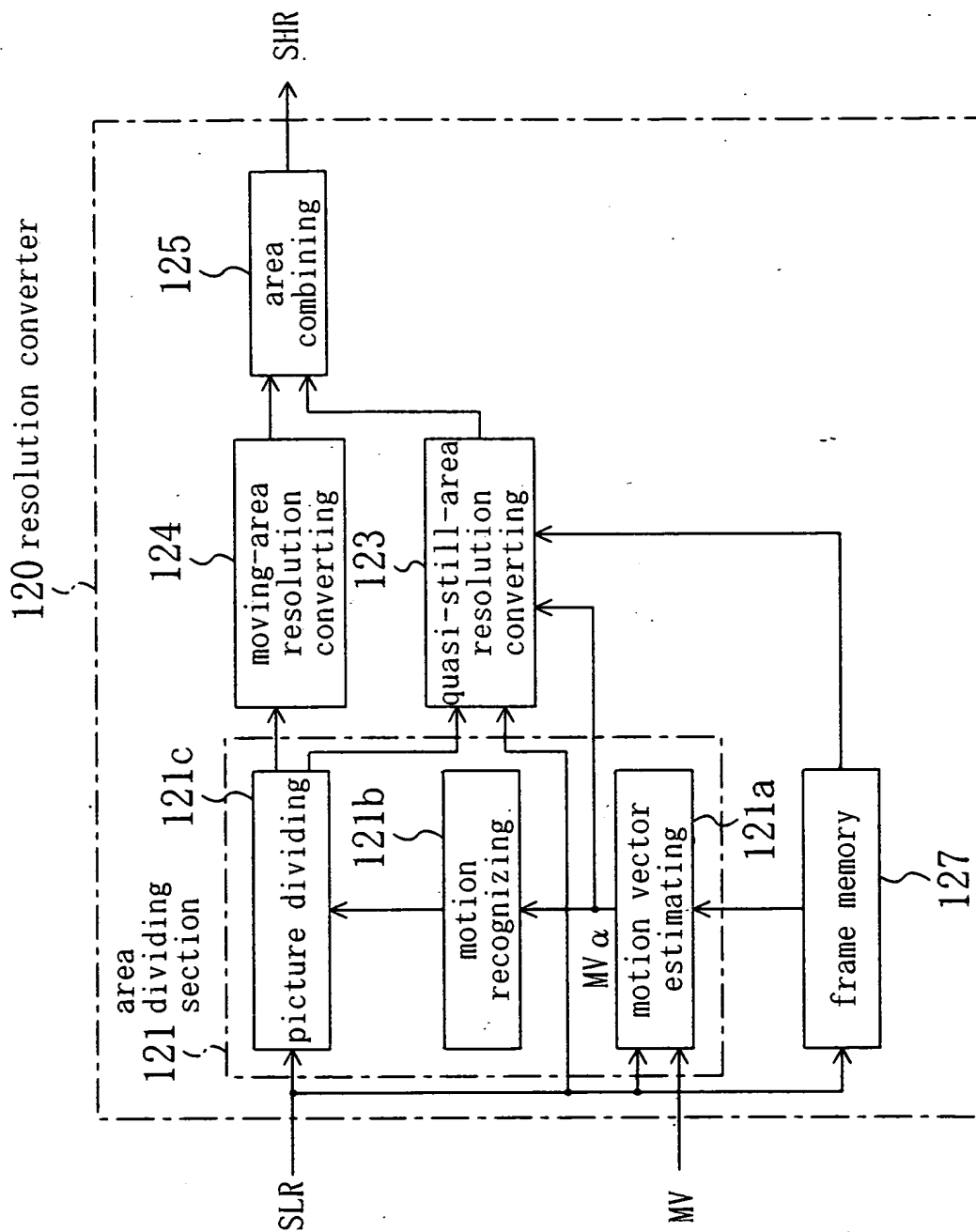


FIG. 10

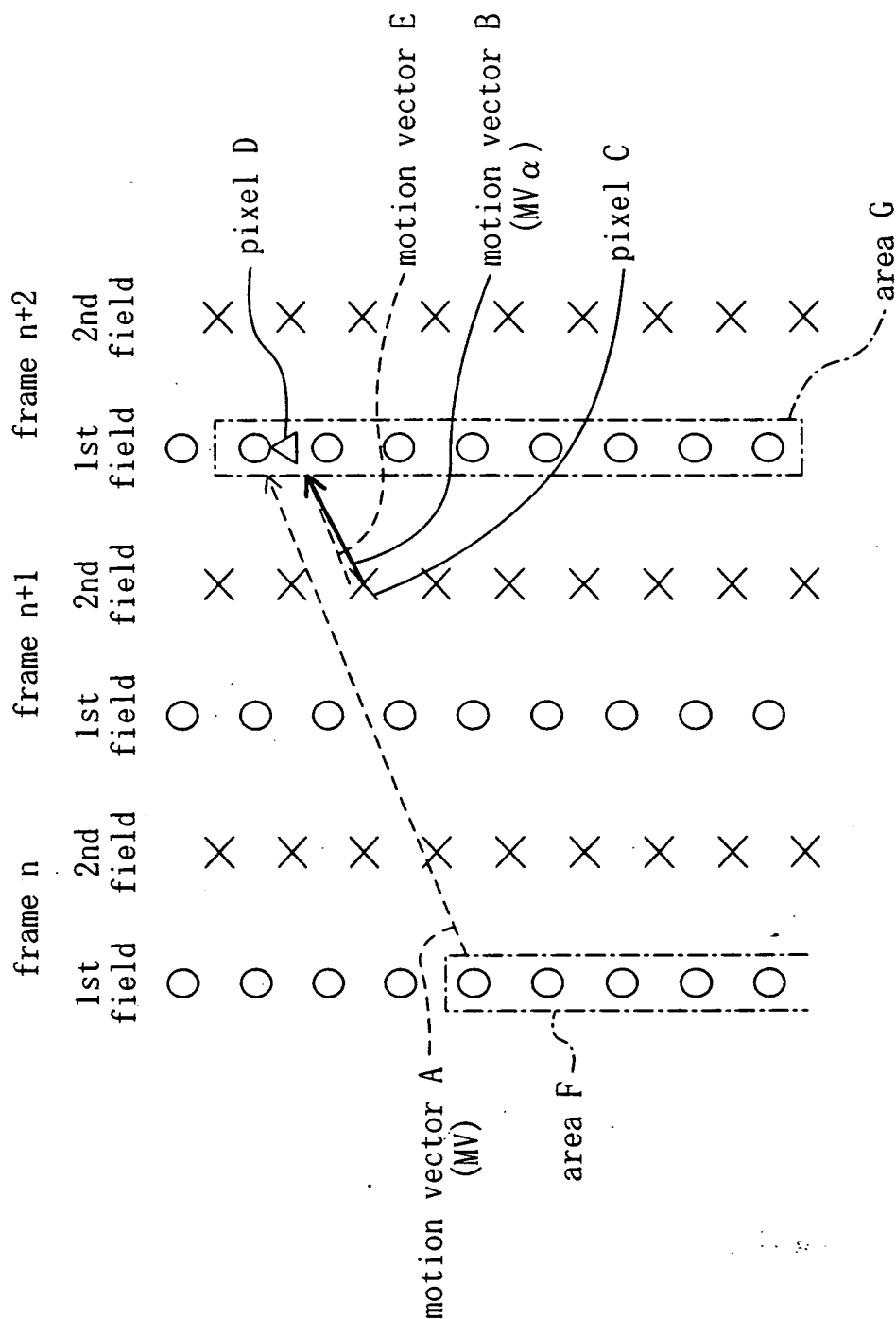


FIG. 11

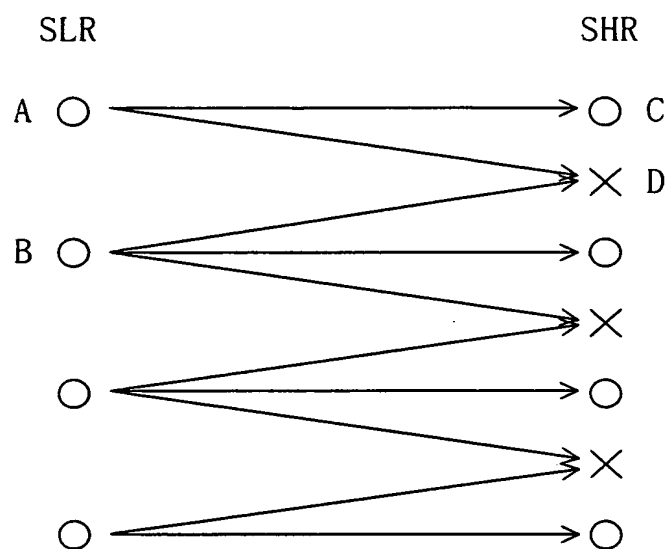


FIG. 12

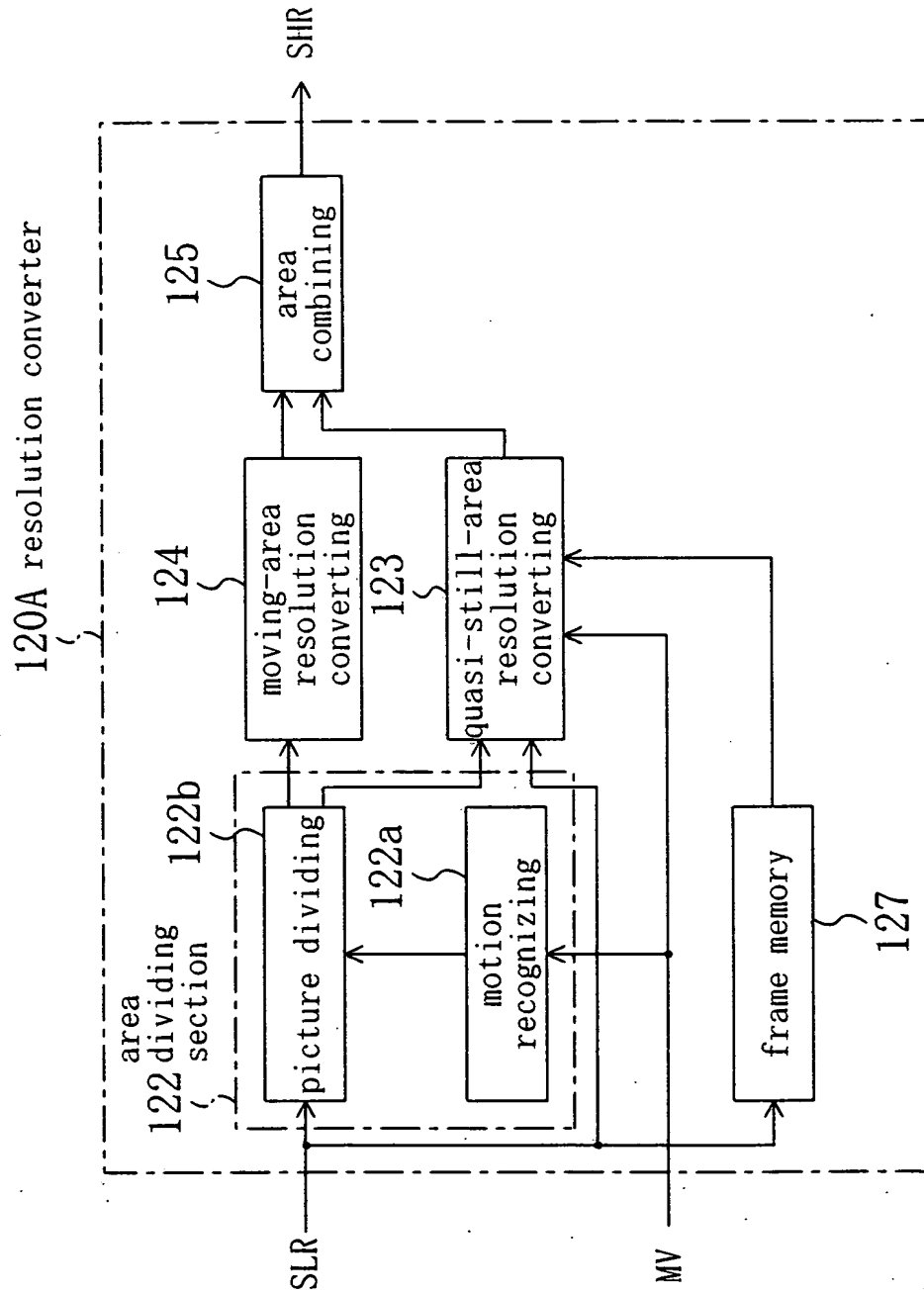
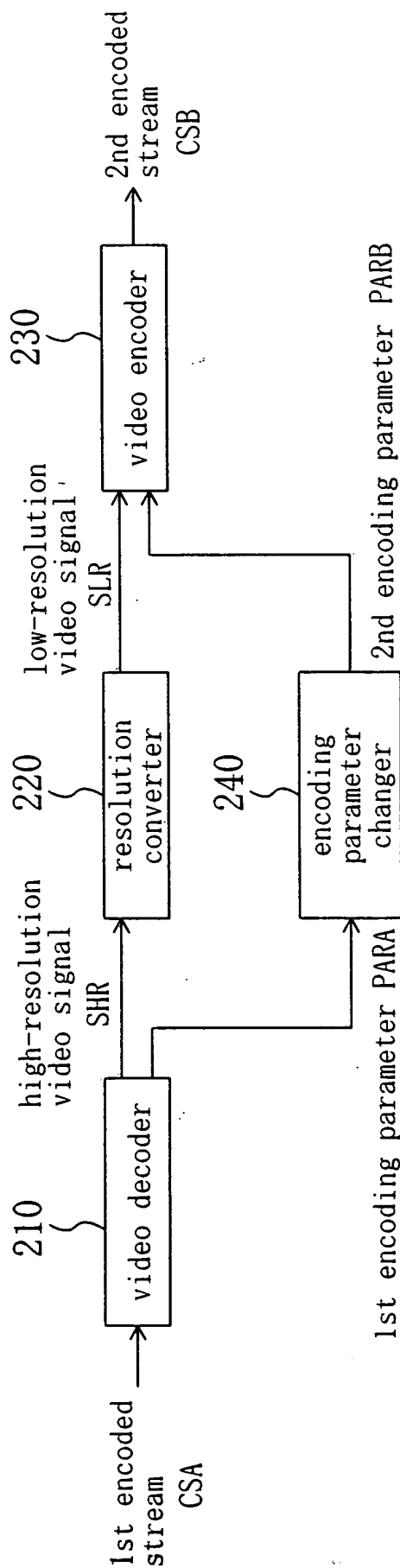
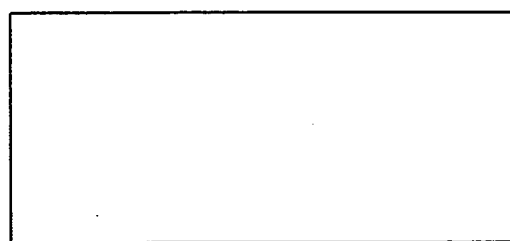


FIG. 13



SHR



SLR

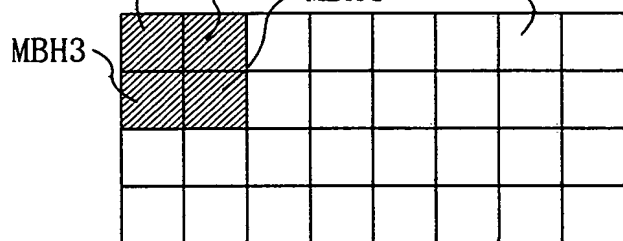


MBH1

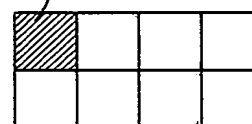
MBH2

- MBH4

macroblock



MBL

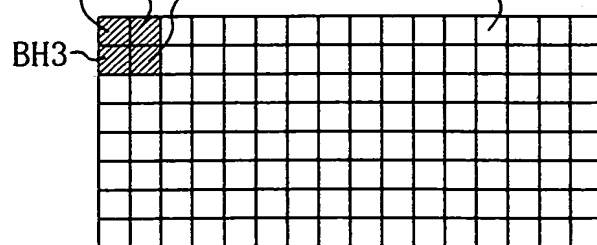


BH1

BH2

BH4

block



BL

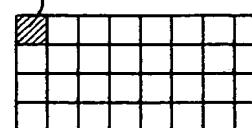


FIG. 15

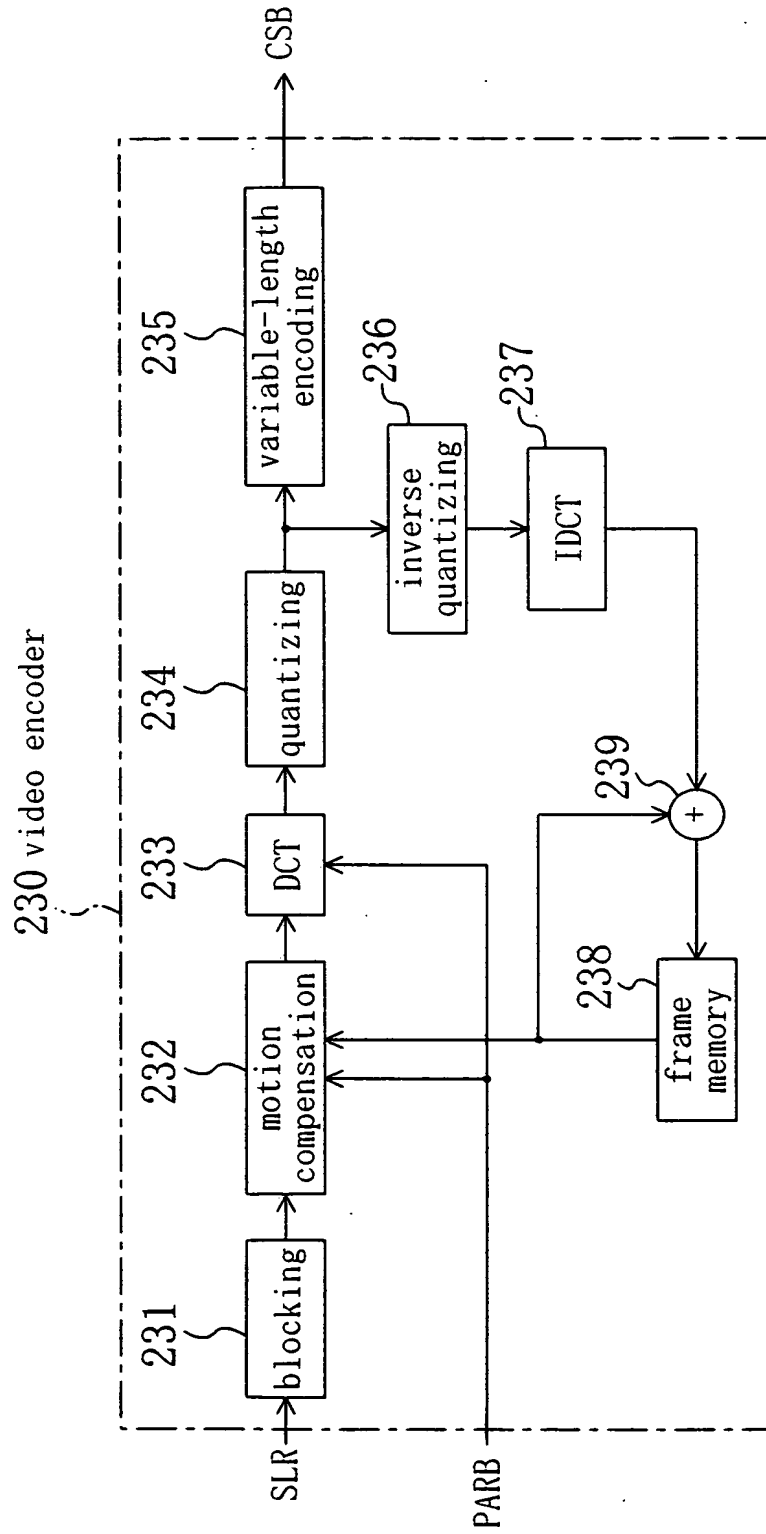


FIG. 16

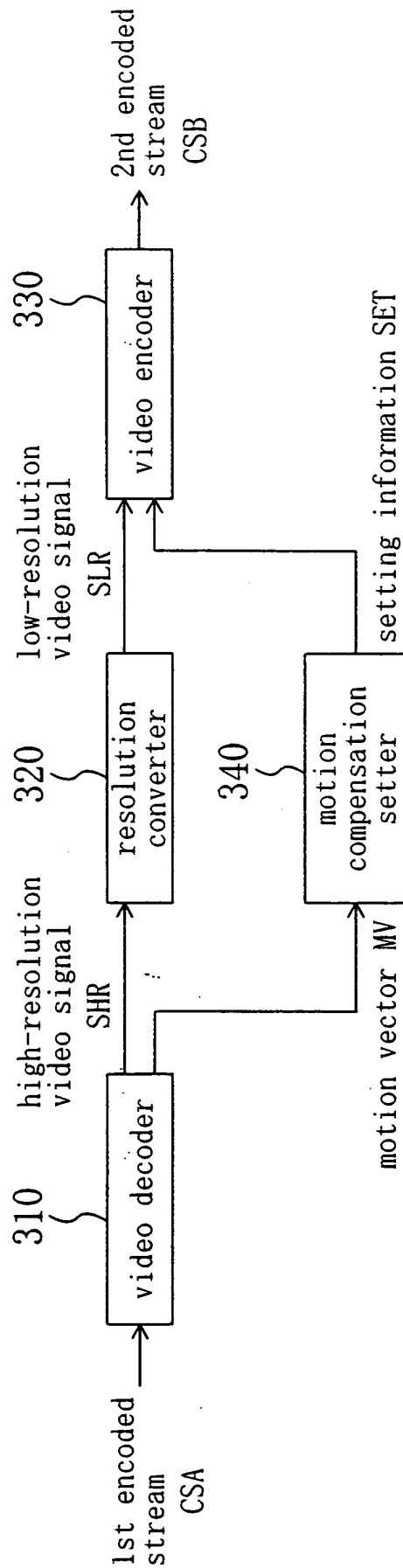


FIG. 17

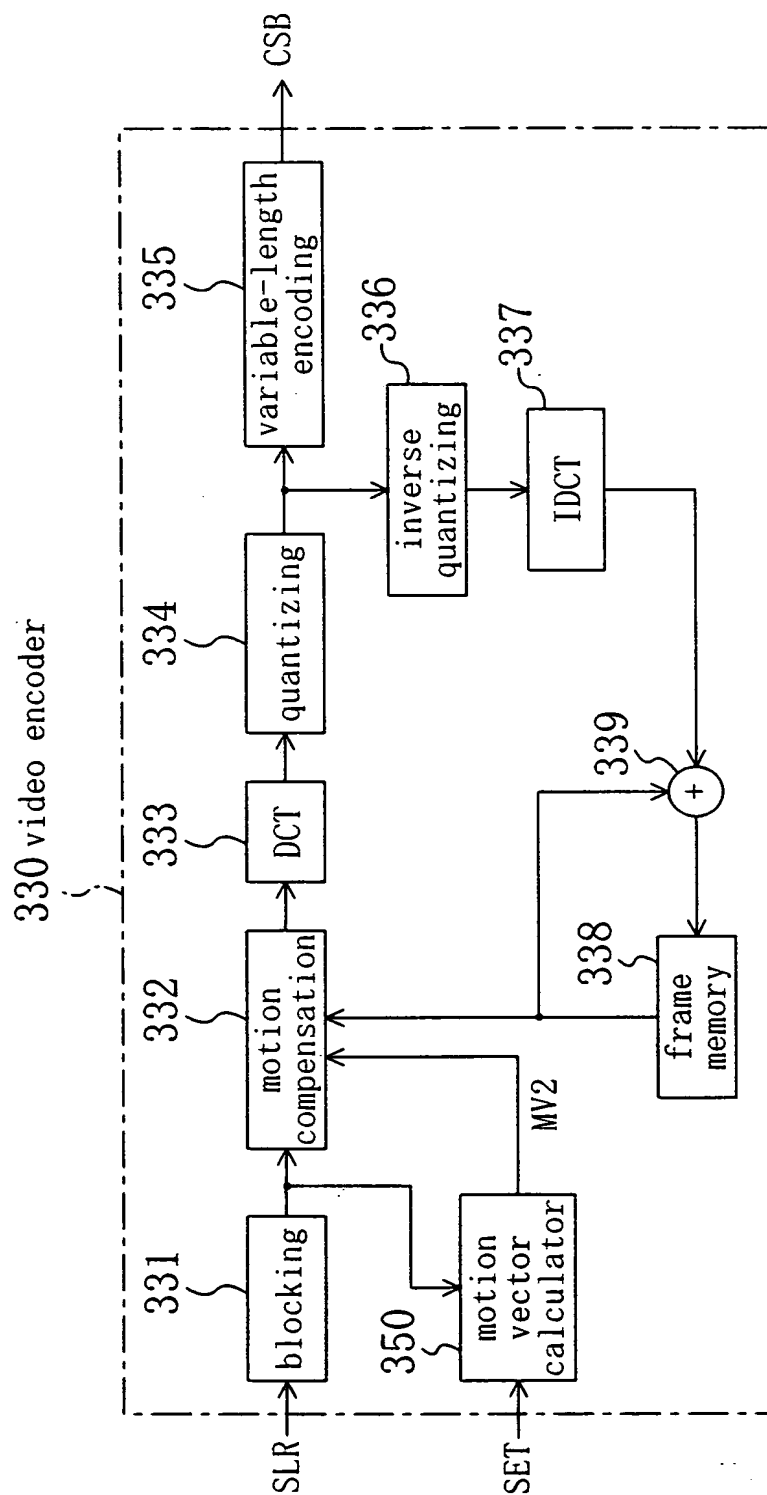


FIG. 18A

frame to
be encoded

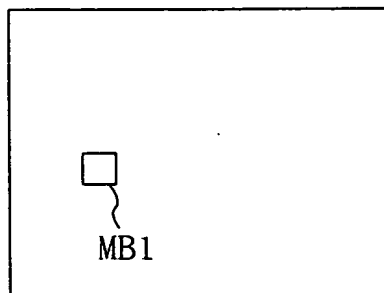


FIG. 18B

reference
frame

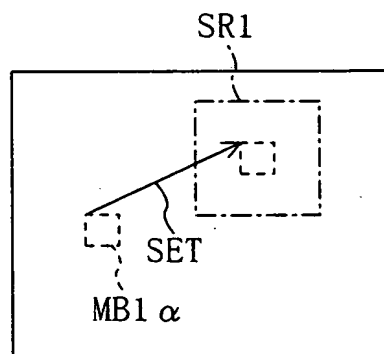
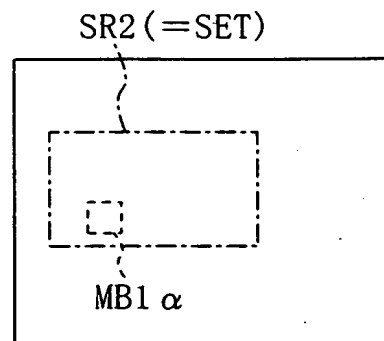


FIG. 18C

reference
frame



09719051-120700

FIG. 19

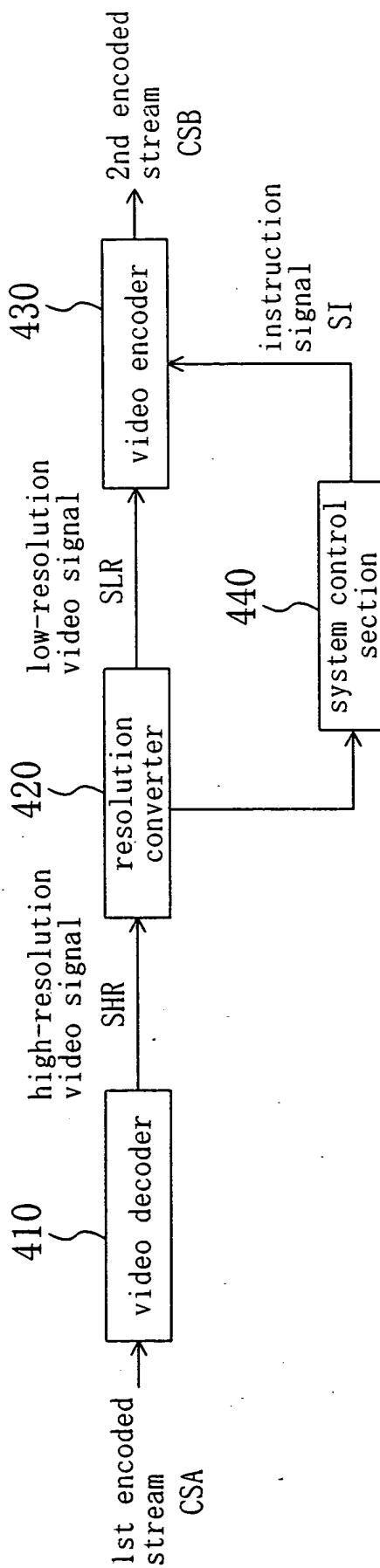
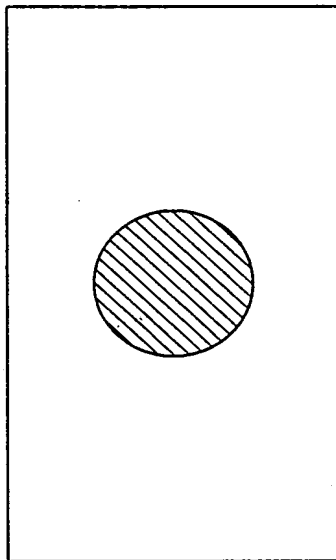


FIG. 20A

high-resolution video signal(16:9)



resolution
conversion
→

FIG. 20B

low-resolution video signal(4:3)

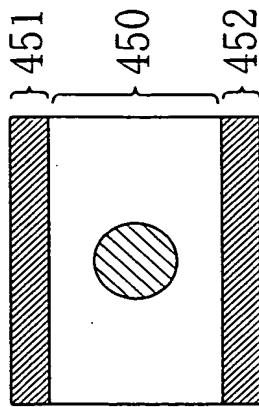


FIG. 21

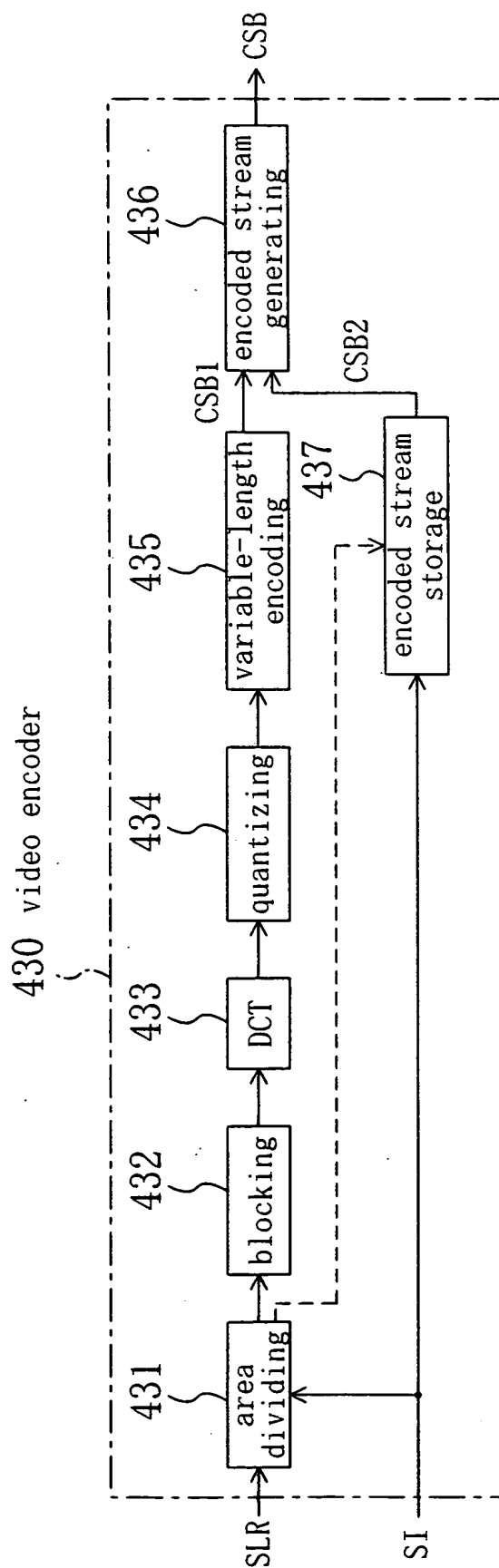


FIG. 22

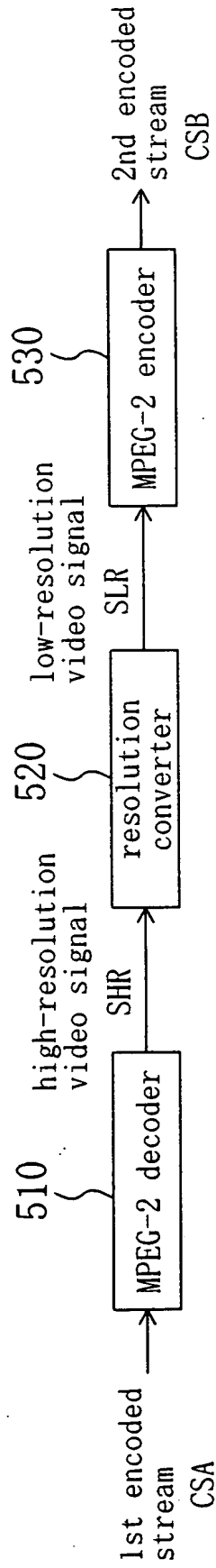


FIG. 23A

frame of
high-resolution
video signal

1st field 2nd field

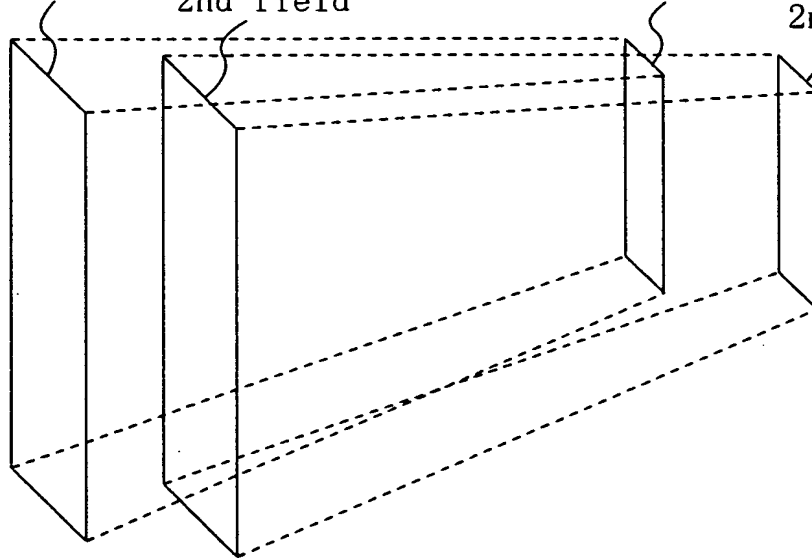


FIG. 23B

frame of
low-resolution
video signal

1st field 2nd field

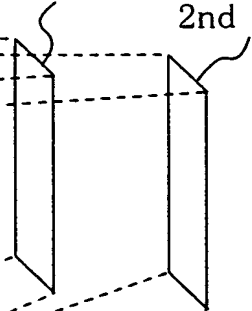


FIG. 24A

high-resolution video signal
(16:9)

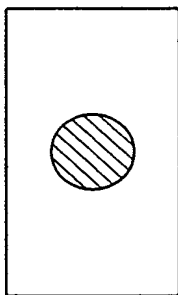


FIG. 24B

low-resolution video signal
(4:3, letterbox)

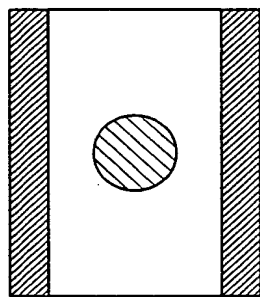


FIG. 24C

low-resolution video signal
(4:3, squeeze)

